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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10064171	FILING DATE 06/18/2002	CLASS 257	SUBCLASS 413	GAU 2845	EXAMINER
<p>**APPLICANTS: Adkisson James; Agnello Paul; Ballantine Arne; Divakaruni Rama; Jones Erin; Rankin Jed; 2811</p> <p>**CONTINUING DATA VERIFIED: THIS APPLICATION IS A DIV OF 09/711,725 11/13/2000</p> <p>** FOREIGN APPLICATIONS VERIFIED:</p>					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no 35 USC 119 conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no Verified and Acknowledged Examiners's initials				ATTORNEY DOCKET NO BUR919990299	
TITLE : Double gate trench transistor					

U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg. Print Fig.
		Application Examiner	
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368, Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.	

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